Product data sheet

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1 General description

The 74AUP1G34 provides a low-power, low-voltage single buffer.

Schmitt trigger action at all inputs makes the circuit tolerant to slower input rise and fall times across the entire V_{CC} range from 0.8 V to 3.6 V.

This device ensures a very low static and dynamic power consumption across the entire V_{CC} range from 0.8 V to 3.6 V.

This device is fully specified for partial power-down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

2 Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- Complies with JEDEC standards:
 - JESD8-12 (0.8 V to 1.3 V)
 - JESD8-11 (0.9 V to 1.65 V)
 - JESD8-7 (1.2 V to 1.95 V)
 - JESD8-5 (1.8 V to 2.7 V)
 - JESD8-B (2.7 V to 3.6 V)
- ESD protection:
 - HBM: ANSI/ESDA/JEDEC JS-001 Class 3A exceeds 5000 V
 - CDM: ANSI/ESDA/JEDEC JS-002 Class C3 exceeds 1000 V
 - MM: JESD22-A115-A exceeds 200 V
- Low static power consumption; I_{CC} = 0.9 μA (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot < 10 % of V_{CC}
- I_{OFF} circuitry provides partial power-down mode operation
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

3 Ordering information

Type number	Package			
	Temperature range	Name	Description	Version
74AUP1G34GW	-40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1
74AUP1G34GM	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1.45 x 0.5 mm	SOT886
74AUP1G34GF	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1 x 0.5 mm	SOT891
74AUP1G34GN	4AUP1G34GN -40 °C to +125 °C XSON6 extremely thin small outline package; no leads; 6 terminals; body 0.9 x 1.0 x 0.35 mm		SOT1115	
74AUP1G34GS	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 1.0 x 1.0 x 0.35 mm	SOT1202
74AUP1G34GX	-40 °C to +125 °C	X2SON5	plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body 0.8 x 0.8 x 0.35 mm	SOT1226
74AUP1G34GX4	-40 °C to +125 °C	X2SON4	plastic thermal enhanced extremely thin small outline package; no leads; 4 terminals; body 0.6 x 0.6 x 0.32 mm	SOT1269-2
74AUP1G34UK	-40 °C to +125 °C	WLCSP6	wafer level chip-scale package; 6 bumps; 0.65 x 0.44 x 0.27 mm	SOT1454-1

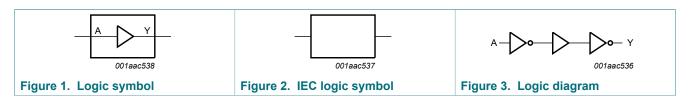
4 Marking

Table 2. Marking	
Type number	Marking code ^[1]
74AUP1G34GW	aN
74AUP1G34GM	aN
74AUP1G34GF	aN
74AUP1G34GN	aN
74AUP1G34GS	aN
74AUP1G34GX	aN
74AUP1G34GX4	aN
74AUP1G34UK	4

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

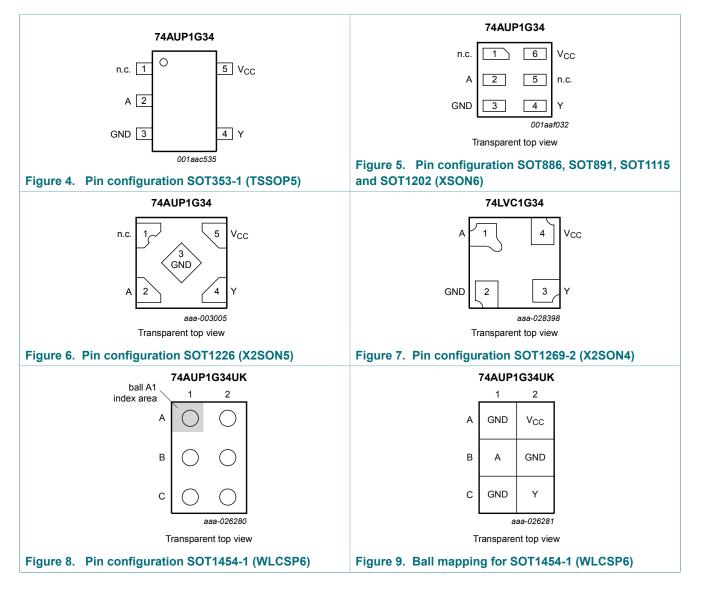
Low-power buffer

5 Functional diagram



6 Pinning information

6.1 Pinning



6.2 Pin description

Symbol	Pin	Pin					
	TSSOP5 and X2SON5	XSON6	X2SON4	WLCSP6			
n.c.	1	1	-	-	not connected		
A	2	2	1	B1	data input		
GND	3	3	2	A1, B2, C1	ground (0 V)		
Y	4	4	3	C2	data output		
n.c.	-	5	-	-	not connected		
V _{CC}	5	6	4	A2	supply voltage		

7 Functional description

Table 4. Function table

H = *HIGH* voltage level; *L* = *LOW* voltage level.

Input	Output
A	Y
L	L
Н	Н

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Limiting values 8

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Мах	Unit
V _{CC}	supply voltage			-0.5	+4.6	V
I _{IK}	input clamping current	V ₁ < 0 V		-50	-	mA
VI	input voltage		[1]	-0.5	+4.6	V
I _{OK}	output clamping current	V _O < 0 V		-50	-	mA
Vo	output voltage	Active mode and Power-down mode	[1]	-0.5	+4.6	V
l _O	output current	$V_{O} = 0 V \text{ to } V_{CC}$		-	±20	mA
I _{CC}	supply current			-	+50	mA
I _{GND}	ground current			-50	-	mA
T _{stg}	storage temperature			-65	+150	°C
P _{tot}	total power dissipation	T_{amb} = -40 °C to +125 °C				
		TSSOP5, XSON6, X2SON5 and WLCSP6 package	[2]	-	250	mW
		X2SON4 package	[3]	-	150	mW

For XSON6 and X2SON5 packages: above 118 °C the value of Ptot derates linearly with 7.8 mW/K.

For WLCSP6 package: above 102.5 °C the value of P_{tot} derates linearly with 5.3 mW/K. [3] For X2SON4 packages: above 57 °C the value of P_{tot} derates linearly with 1.7 mW/K.

Recommended operating conditions 9

Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Мах	Unit
V _{CC}	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
Vo	output voltage	Active mode	0	V _{CC}	V
		Power-down mode; V_{CC} = 0 V	0	3.6	V
T _{amb}	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	V _{CC} = 0.8 V to 3.6 V	0	200	ns/V

10 Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = 2	5 °C	1				
V _{IH}	HIGH-level input	V _{CC} = 0.8 V	0.70 × V _{CC}	-	-	V
	voltage	V _{CC} = 0.9 V to 1.95 V	0.65 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input	V _{CC} = 0.8 V	-	-	0.30 × V _{CC}	V
	voltage	V _{CC} = 0.9 V to 1.95 V	-	-	$0.35 \times V_{CC}$	V
-		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OH}	HIGH-level output	$V_{I} = V_{IH} \text{ or } V_{IL}$				
	voltage	I_{O} = -20 µA; V_{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.75 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.11	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.32	-	-	V
		I _O = -2.3 mA; V _{CC} = 2.3 V	2.05	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.9	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.72	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.6	-	-	V
V _{OL}	LOW-level output	$V_{I} = V_{IH} \text{ or } V_{IL}$				
	voltage	I_{O} = 20 µA; V_{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	$0.3 \times V_{CC}$	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.31	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.31	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.31	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.44	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.31	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.44	V
lı	input leakage current	V_I = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	±0.1	μA
I _{OFF}	power-off leakage current	V_{I} or V_{O} = 0 V to 3.6 V; V_{CC} = 0 V	-	-	±0.2	μA
ΔI _{OFF}	additional power-off leakage current	V_{I} or V_{O} = 0 V to 3.6 V; V_{CC} = 0 V to 0.2 V	-	-	±0.2	μA

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Low-power buffer

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
I _{CC}	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.5	μA
ΔI _{CC}	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$ ^[1]	-	-	40	μA
CI	input capacitance	V_{CC} = 0 V to 3.6 V; V_{I} = GND or V_{CC}	-	0.8	-	pF
Co	output capacitance	$V_{O} = GND; V_{CC} = 0 V$	-	1.7	-	pF
T _{amb} = -4	0 °C to +85 °C		11		-1	
V _{IH}	HIGH-level input	V _{CC} = 0.8 V	0.70 × V _{CC}	-	-	V
	voltage	V _{CC} = 0.9 V to 1.95 V	0.65 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input	V _{CC} = 0.8 V	-	-	$0.30 \times V_{CC}$	V
	voltage	V _{CC} = 0.9 V to 1.95 V	-	-	0.35 × V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	Image: Properties of the second se	V		
V _{OH}	HIGH-level output	$V_{I} = V_{IH} \text{ or } V_{IL}$				
Y	voltage	I_{O} = -20 µA; V_{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.7 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.03	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.30	-	-	V
		I_{O} = -2.3 mA; V_{CC} = 2.3 V	1.97	-	-	V
		I_{O} = -3.1 mA; V_{CC} = 2.3 V	1.85	-	-	V
		$I_{\rm O}$ = -2.7 mA; $V_{\rm CC}$ = 3.0 V	2.67	-	-	V
		I_{O} = -4.0 mA; V_{CC} = 3.0 V	2.55	-	-	V
V _{OL}	LOW-level output	$V_{I} = V_{IH} \text{ or } V_{IL}$				
	voltage	I_{O} = 20 µA; V_{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.37	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.35	V
		$I_{\rm O}$ = 2.3 mA; $V_{\rm CC}$ = 2.3 V	-	-	0.33	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.45	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.33	V
		I_{O} = 4.0 mA; V_{CC} = 3.0 V	-	-	0.45	V
I	input leakage current	V_{I} = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	±0.5	μA
I _{OFF}	power-off leakage current	V_1 or V_0 = 0 V to 3.6 V; V_{CC} = 0 V	-	-	±0.5	μA

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Low-power buffer

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Δl _{OFF}	additional power-off leakage current	V_{I} or V_{O} = 0 V to 3.6 V; V_{CC} = 0 V to 0.2 V	-	-	±0.6	μA
I _{CC}	supply current	V_1 = GND or V_{CC} ; I_0 = 0 A; V_{CC} = 0.8 V to 3.6 V	-	-	0.9	μA
ΔI _{CC}	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$ ^[1]	-	-	50	μA
T _{amb} = -4	0 °C to +125 °C		· · ·			
V _{IH}	HIGH-level input	V _{CC} = 0.8 V	$0.75 \times V_{CC}$	-	-	V
	voltage	V _{CC} = 0.9 V to 1.95 V	$0.70 \times V_{CC}$	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input	V _{CC} = 0.8 V	-	-	0.25 × V _{CC}	V
	voltage	V _{CC} = 0.9 V to 1.95 V	-	-	0.30 × V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OH}	HIGH-level output	$V_{I} = V_{IH} \text{ or } V_{IL}$				
	voltage	$I_{\rm O}$ = -20 $\mu \text{A}; \text{V}_{\text{CC}}$ = 0.8 V to 3.6 V	V _{CC} - 0.11	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	$0.6 \times V_{CC}$	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	0.93	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.17	-	-	V
		$I_{\rm O}$ = -2.3 mA; $V_{\rm CC}$ = 2.3 V	1.77	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.67	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.40	-	-	V
		I_{O} = -4.0 mA; V_{CC} = 3.0 V	2.30	-	-	V

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Low-power buffer

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{OL}	LOW-level output	V _I = V _{IH} or V _{IL}				
	voltage	I_{O} = 20 µA; V_{CC} = 0.8 V to 3.6 V	-	-	0.11	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.33 × V _{CC}	V
	I _O = 1.7 mA; V _{CC} = 1.4 V		-	-	0.41	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.39	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.36	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.50	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.36	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.50	V
lı	input leakage current	V_I = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	±0.75	μA
I _{OFF}	power-off leakage current	V_{I} or V_{O} = 0 V to 3.6 V; V_{CC} = 0 V	-	-	±0.75	μA
ΔI _{OFF}	additional power-off leakage current	V_{I} or V_{O} = 0 V to 3.6 V; V_{CC} = 0 V to 0.2 V	-	-	±0.75	μA
I _{CC}	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	1.4	μA
ΔI _{CC}	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$ ^[1]	-	-	75	μA

[1] One input at V_{CC} - 0.6 V, other input at V_{CC} or GND.

11 Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 11.

Symbol	Parameter	Conditions	T,	amb = 25 °	°C	Т	amb = -40 °	C to +1:	25 °C	Unit
			Min	Typ ^[1]	Мах	Min	Мах (85 °С)	Min	Max (125 °C)	
C _L = 5 pF	•					1	1		1	
t _{pd}	propagation	A to Y; see <u>Figure 10</u> ^[2]								
	delay	V _{CC} = 0.8 V	-	15.0	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.6	4.7	9.2	2.0	10.0	2.0	11.0	ns
		V _{CC} = 1.4 V to 1.6 V	2.1	3.4	5.7	1.6	6.5	1.6	7.2	ns
		V _{CC} = 1.65 V to 1.95 V	1.8	2.9	4.5	1.4	5.2	1.4	5.8	ns
		V_{CC} = 2.3 V to 2.7 V	1.5	2.3	3.5	1.2	4.2	1.2	4.6	ns
		V_{CC} = 3.0 V to 3.6 V	1.4	2.1	3.2	1.0	3.8	1.0	4.2	ns
C _L = 10 p	F	<u> </u>					1	1		
	propagation	A to Y; see <u>Figure 10</u> ^[2]								
	delay	V _{CC} = 0.8 V	-	18.4	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.2	5.6	10.9	2.3	11.8	2.3	13.1	ns
		V _{CC} = 1.4 V to 1.6 V	2.6	4.1	6.7	1.9	7.7	1.9	8.5	ns
		V _{CC} = 1.65 V to 1.95 V	2.3	3.4	5.3	1.7	6.2	1.7	6.9	ns
		V_{CC} = 2.3 V to 2.7 V	2.0	2.9	4.2	1.5	5.0	1.5	5.5	ns
		V _{CC} = 3.0 V to 3.6 V	1.7	2.6	3.8	1.4	4.6	1.4	5.1	ns
C _L = 15 p	F	<u> </u>						1		
t _{pd}	propagation	A to Y; see <u>Figure 10</u> ^[2]								
	delay	V _{CC} = 0.8 V	-	21.9	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.6	6.4	12.6	2.6	13.8	2.6	15.2	ns
		V _{CC} = 1.4 V to 1.6 V	3.0	4.6	7.6	2.2	8.9	2.2	9.8	ns
		V _{CC} = 1.65 V to 1.95 V	2.6	3.9	6.0	2.0	7.2	2.0	7.9	ns
		V_{CC} = 2.3 V to 2.7 V	2.3	3.3	4.8	1.8	5.7	1.8	6.3	ns
		V _{CC} = 3.0 V to 3.6 V	2.1	3.1	4.2	1.6	5.0	1.6	5.5	ns

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Low-power buffer

Symbol	Parameter	Conditions	T,	_{amb} = 25 °	°C	Т	amb = -40 °	C to +1	25 °C	Unit
			Min	Typ ^[1]	Мах	Min	Max (85 °C)	Min	Max (125 °C)	
C _L = 30 p	р F	1		1		1	1	1	1	
t _{pd}	propagation	A to Y; see <u>Figure 10</u> ^[2]								
	delay	V _{CC} = 0.8 V	-	32.1	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	4.8	8.7	16.3	3.6	18.9	3.6	20.8	ns
		V _{CC} = 1.4 V to 1.6 V	4.0	6.2	10.3	3.4	12.2	3.4	13.4	ns
		V _{CC} = 1.65 V to 1.95 V	3.6	5.2	8.1	3.2	9.8	3.2	10.8	ns
		V_{CC} = 2.3 V to 2.7 V	3.0	4.4	6.4	2.7	7.7	2.7	8.5	ns
		V _{CC} = 3.0 V to 3.6 V	2.9	4.2	5.6	2.5	6.5	2.5	7.2	ns
C _{PD}	power dissipation	V_{I} = GND to V_{CC} ; ^[3] f_{i} = 1 MHz								
	capacitance	V _{CC} = 0.8 V	-	2.5	-	-	-	-	-	pF
		V _{CC} = 1.1 V to 1.3 V	-	2.6	-	-	-	-	-	pF
		V _{CC} = 1.4 V to 1.6 V	-	2.7	-	-	-	-	-	pF
		V _{CC} = 1.65 V to 1.95 V	-	2.9	-	-	-	-	-	pF
		V_{CC} = 2.3 V to 2.7 V	-	3.4	-	-	-	-	-	pF
		V _{CC} = 3.0 V to 3.6 V	-	4.0	-	-	-	-	-	pF

[1] All typical values are measured at nominal V_{CC}.

[2] t_{pd} is the same as t_{PLH} and t_{PHL} . [3] C_{PD} is used to determine the dynamic power dissipation (P_D in μ W).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$ where:

 f_i = input frequency in MHz;

 f_o = output frequency in MHz;

C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_0)$ = sum of outputs.

11.1 Waveform and test circuit

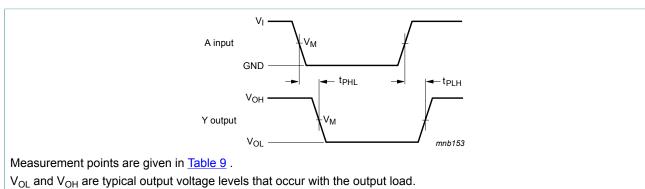
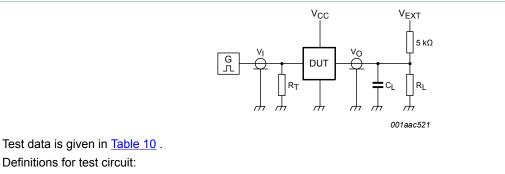


Figure 10. The data input (A) to output (Y) propagation delays

Table 9. Measurement points

Supply voltage	Output	Input				
V _{cc}	V _M	V _M	VI	t _r = t _f		
0.8 V to 3.6 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	V _{CC}	≤ 3.0 ns		



R_L = Load resistance.

 C_L = Load capacitance including jig and probe capacitance.

 R_T = Termination resistance should be equal to the output impedance Z_0 of the pulse generator.

 V_{EXT} = External voltage for measuring switching times.

Figure 11. Test circuit for measuring switching times

Table 10. Test data

Supply voltage	Load		V _{EXT}				
V _{cc}	CL	R _L ^[1]	t _{PLH} , t _{PHL}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}		
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	2 × V _{CC}		

[1] For measuring enable and disable times $R_L = 5 k\Omega$, for measuring propagation delays, setup and hold times and pulse width $R_L = 1 M\Omega$.

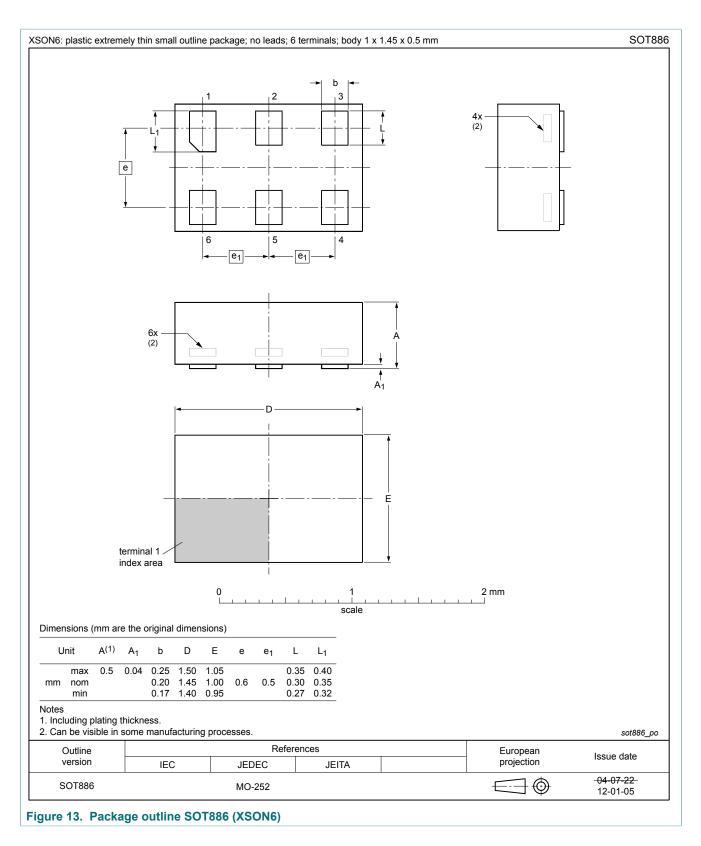
74AUP1G34 Product data sheet

74AUP1G34 Low-power buffer

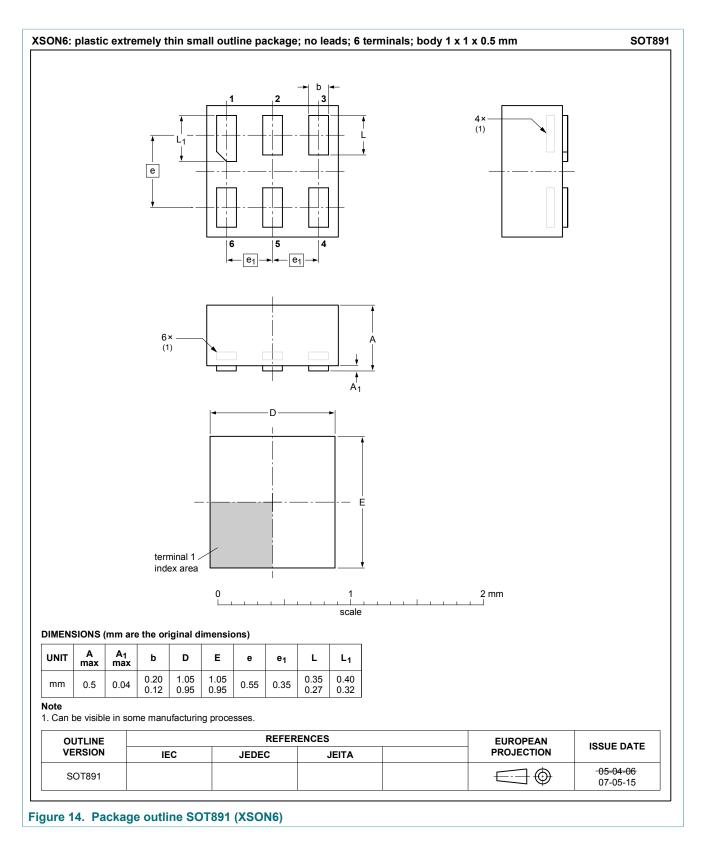
12 Package outline

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UNIT mm lote	A max.	A₁ 0.1 0	A ₂ 1.0 0.8	inal din A3 0.15	0 0 0.30 0.15	s) c 0.25 0.08	1.5 scal D(1) 2.25 1.85	E(1) 1.35 1.15	0.65	e ₁	2.25	L	L р 0.46				0.60	7°
UNIT mm lote . Plastic	A max. 1.1 c or meta	A₁ 0.1 0	A2 1.0 0.8	inal din A 3 0.15	0 0 0.30 0.15	s) c 0.25 0.08	1.5 scal D(1) 2.25 1.85 side are	E(1) 1.35 1.15	0.65 Sluded.	e ₁	2.25	L	Lp 0.46 0.21	0.3	0.1 PEAN	0.1	0.60 0.15	7° 0°
UNIT mm lote . Plastic VE	A max. 1.1 c or meta	A₁ 0.1 0	A2 1.0 0.8	inal din A3 0.15	0 0 0.30 0.15	s) c 0.25 0.08	1.5 scal D(1) 2.25 1.85 side are REFEF	E(1) 1.35 1.15 e not inc RENCES	0.65 luded.	e 1 1.3	2.25	L	Lp 0.46 0.21	0.3	0.1 PEAN	0.1	0.60	7° 0°

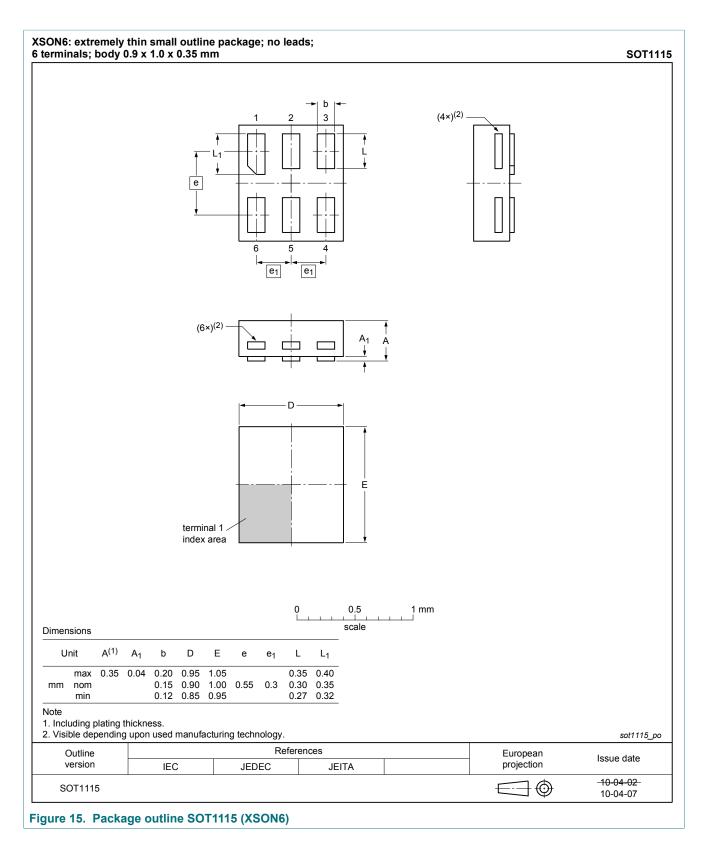
Low-power buffer



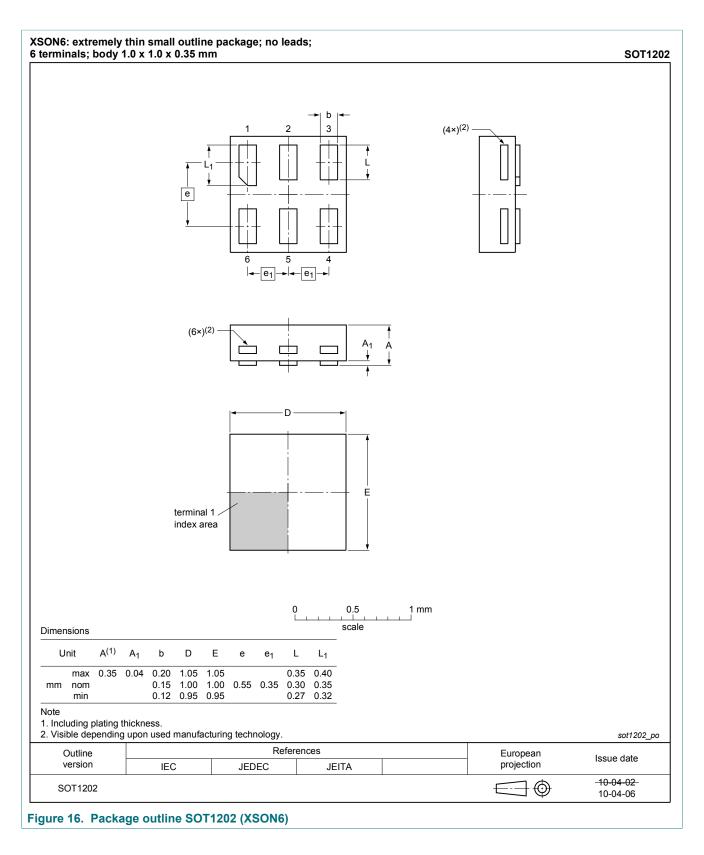
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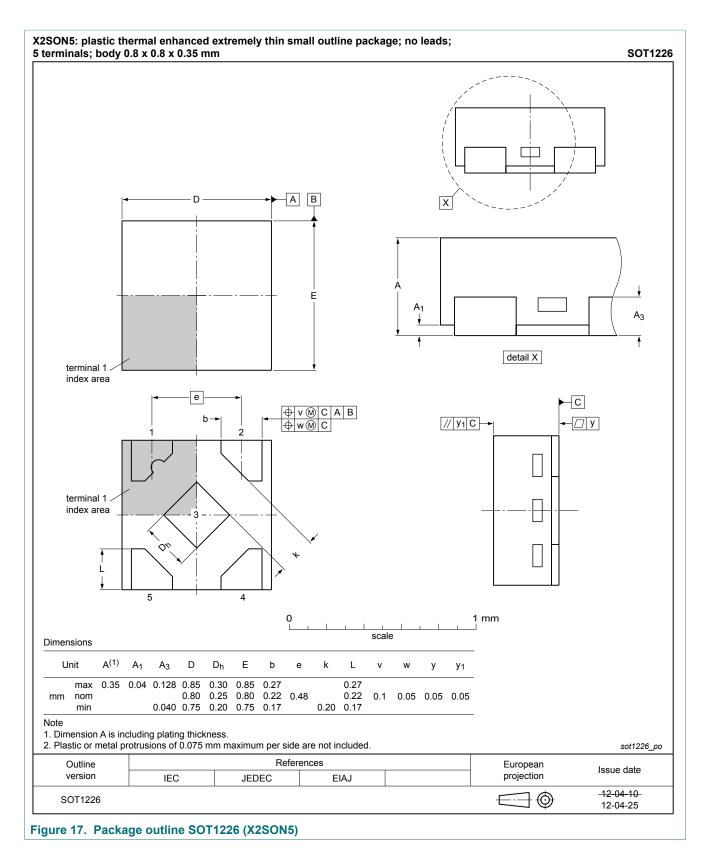
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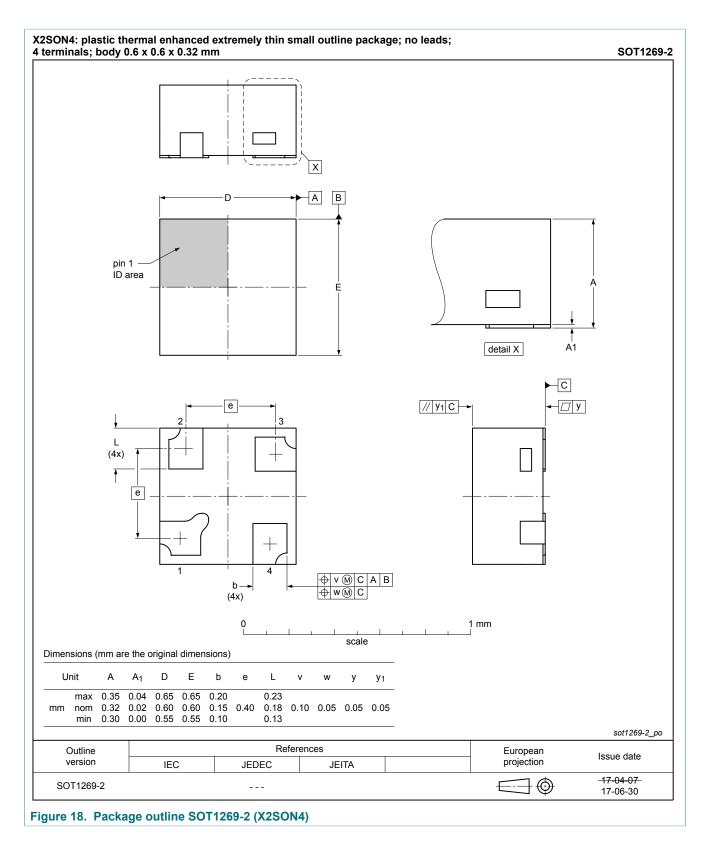
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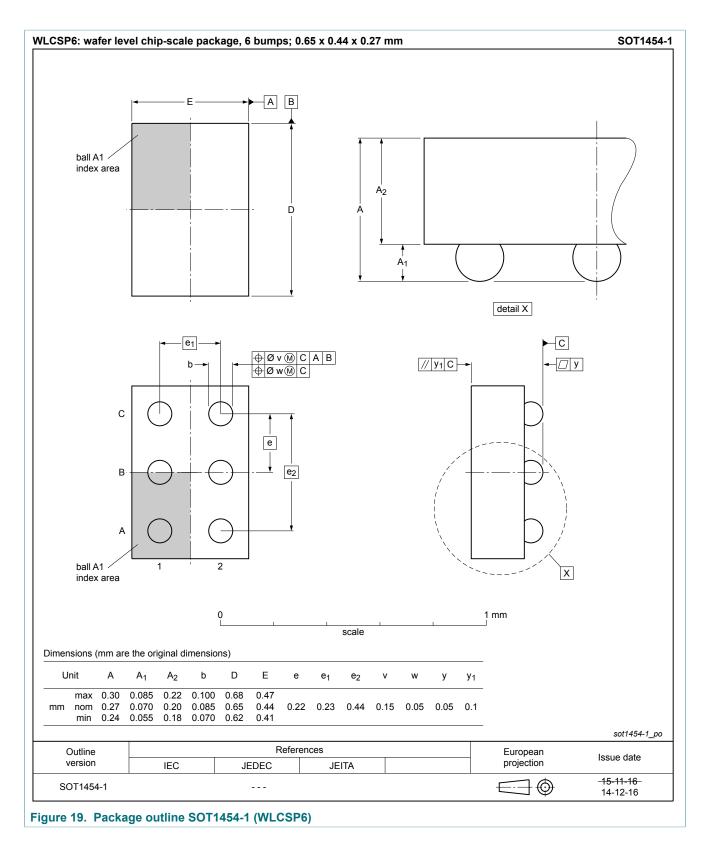
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13 Abbreviations

Table 11. Abbreviations					
Acronym	Description				
CDM	Charged Device Model				
DUT	Device Under Test				
ESD	ElectroStatic Discharge				
НВМ	Human Body Model				
MM	Machine Model				

14 Revision history

Table 12. Revision	history								
Document ID	Release date	Data sheet status	Change notice	Supersedes					
74AUP1G34 v.8	20180608	Product data sheet	-	74AUP1G34 v.7					
Modifications:	 Added type nur 	Added type number 74AUP1G34GX4 (SOT1269-2)							
74AUP1G34 v.7	20170328	Product data sheet	-	74AUP1G34 v.6					
Modifications:	 Added type nur 	mber 74AUP1G34UK (SOT14	154-1/WLCSP6)						
74AUP1G34 v.6	20120628	Product data sheet	-	74AUP1G34 v.5					
Modifications:	51	 Added type number 74AUP1G34GX (SOT1226) Package outline drawing of SOT886 (Figure 13) modified. 							
74AUP1G34 v.5	20111128	Product data sheet	-	74AUP1G34 v.4					
Modifications:	 Legal pages up 	odated.	"						
74AUP1G34 v.4	20100714	Product data sheet	-	74AUP1G34 v.3					
74AUP1G34 v.3	20080814	Product data sheet	-	74AUP1G34 v.2					
74AUP1G34 v.2	20060704	Product data sheet	-	74AUP1G34 v.1					
74AUP1G34 v.1	20050804	Product data sheet	-	-					

15 Legal information

15.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

Please consult the most recently issued document before initiating or completing a design. [1]

The term 'short data sheet' is explained in section "Definitions".

[2] [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nexperia.com.

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